

Notice of References Cited	Application/Control No. 10/628,074		Applicant(s)/Patent Under Reexamination MOACANIN ET AL.	
	Examiner Cynthia Britt		Art Unit 2138	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,900,753 A	05-1999	Cote et al.	327/145
*	B	US-6,980,943 B2	12-2005	Aitken et al.	703/14
*	C	US-7,007,215 B2	02-2006	Kinoshita et al.	714/745
*	D	US-7,058,535 B2	06-2006	Chenoweth et al.	702/118
*	E	US-6,807,644 B2	10-2004	Reis et al.	714/724
*	F	US-2002/0152046 A1	10-2002	Velichko et al.	702/117
*	G	US-6,848,060 B2	01-2005	Cook et al.	713/400
*	H	US-5,958,077 A	09-1999	Banerjee et al.	714/738
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Low-power synchronous-to-asynchronous- to-synchronous interlocked pipelined CMOS circuits operating at 3.3-4.5 GHz" by Schuster et al. This paper appears in: IEEE Journal of Solid-State Circuits Publication Date: Apr 2003 Volume: 38, Issue: 4 On page(s): 622- 630 ISSN: 0018-9200 INSPEC Accession Number: 7565233
	V	IBM TDB: NN8607813 "Method to Synchronize an Asynchronous IBM Series/1 Interface to Allow Testing on a Synchronous Tester" IBM Technical Disclosure Bulletin, July 1986, US VOLUME:29, ISSUE:2 PAGE: 813 - 815 Publication Date: July 1, 1986 (19860701)
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.